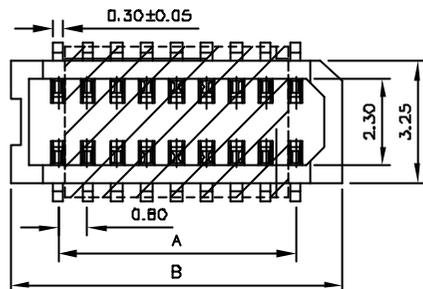


CIRCUITS	DIM A	DIM B	DIM C	REMARK (備註) 有請註明
10	3.20	5.75	4.75	
12	4.00	6.55	5.55	
14	4.80	7.35	6.35	
16	5.60	8.15	7.15	
18	6.40	8.95	7.95	
20	7.20	9.75	8.75	
22	8.00	10.55	9.55	
24	8.80	11.35	10.35	⊕
26	9.60	12.15	11.15	⊕
28	10.40	12.95	11.95	
30	11.20	13.75	12.75	
32	12.00	14.55	13.55	
34	12.80	15.35	14.35	
36	13.60	16.15	15.15	
40	15.20	17.75	16.75	
50	19.20	21.75	20.75	
60	23.20	25.75	24.75	
70	27.20	29.75	28.75	
80	31.20	33.75	32.75	
90	35.20	37.75	36.75	
100	39.20	41.75	40.75	

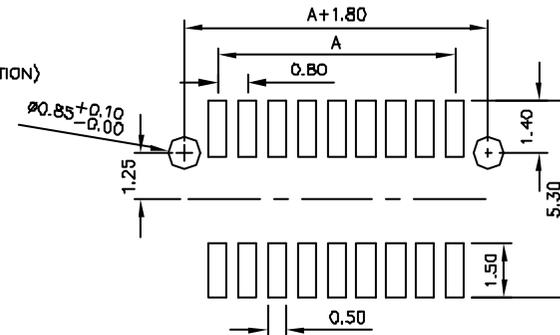
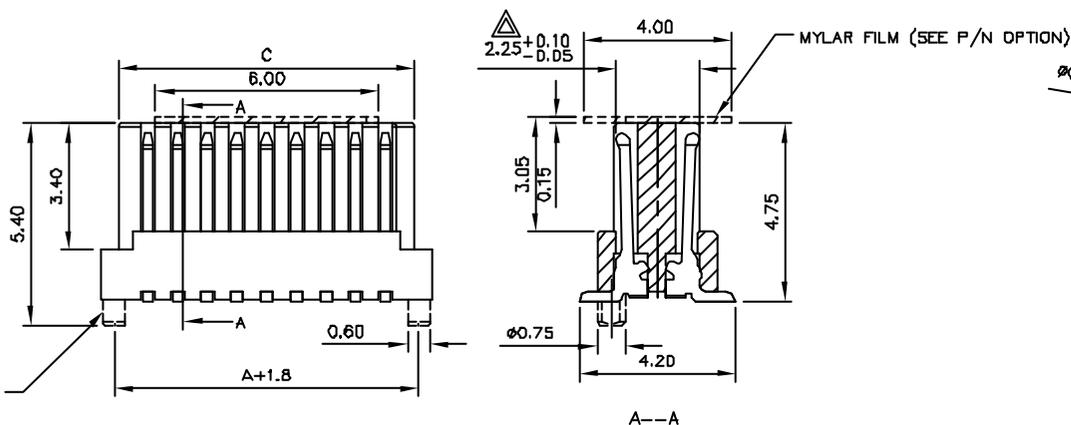


ZIW-XXS4-X-X-X  
 Number of Contacts  
 0: W/O Locating peg  
 1: With Locating peg  
 Contact Plating

Definition	Code
⊕ Tin plated:	A
⊕ Gold plated:	
flash B 15μ"	F
10μ" E 30μ" J	
⊕ Duplex plating:	
flash K 15μ" P	
10μ" N 30μ" U	
⊕ Standard: B	

SPECIFICATIONS

Current Rating: 0.5 Amps  
 Insulator resistance: 100 Megohms Min.  
 Contact resistance: 70 m ohm Max.  
 Dielectric withstanding: AC 500 V  
 Operating Temperature: -40°C~+85°C  
 Contact Material: Phosphor bronze  
 Insulator Material: High Temperature, UL 94V-0 Thermoplastic  
 Color: Natural  
 Finish: Gold all over nickel



Recommended PCB Layout  
 (PCB TOLERANCE ±0.05)

Rev.	ECN Number	Description	Drawn	Date
2	DOC.9210361	客戶要求修公差	林秋蘭	03'/10/28



GENERAL TOLERANCE		SCALE 1=1	ORIGINAL DRAWN 許益昌	DATE 03'/09/19
Dimension	Angle	UNIT MM	CHECK 江坤容	DATE 03'/10/28
±0.20	4°	SIZE A4/5	APPROVE 李國寶	DATE 03'/10/28

DWG. NO. 4-03	TITLE 0.8mm Board To Board Pulg Type 2 (成品規格圖)	REV. 2
F:\DWG\CDWG\ZIW\ZIWMOM.DWG		
PARTS NO. ZIW-XXS4-X-X-X		SHEET 1/1